

Freeform Search

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

115 and 118

Termi-

Display: **15** Documents in Display Format: **-** Starting with Number **1**

Generate: Hit List Hit Count Side by Side Image

Search | **Clear** | **Interrupt**

Search History

DATE: Sunday, December 19, 2004 [Printable Copy](#) [Create Case](#)

| <u>Set</u> | <u>Hit</u> | <u>Set</u> |
|---|--------------|-------------|
| <u>Name</u> | <u>Count</u> | <u>Name</u> |
| side by side | | result set |
| <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | |
| <u>L19</u> 115 and L18 | 5 | <u>L19</u> |
| <u>L18</u> 20010039610 or (6243793 or 6065102 or 5913227).pn. | 8 | <u>L18</u> |
| <u>L17</u> 115 with behlf | 0 | <u>L17</u> |
| <u>L16</u> 11 and L15 | 2 | <u>L16</u> |
| <u>L15</u> master with slave with l8 | 430 | <u>L15</u> |
| <u>L14</u> 11 and L13 | 3 | <u>L14</u> |
| <u>L13</u> l8 and l11 and L12 | 583 | <u>L13</u> |
| <u>L12</u> master near1 processor | 3662 | <u>L12</u> |
| <u>L11</u> multi\$1process\$5 | 40634 | <u>L11</u> |
| <u>L10</u> 11 and L9 | 12 | <u>L10</u> |
| <u>L9</u> l7 and L8 | 1800 | <u>L9</u> |
| <u>L8</u> (common or shar\$5) near5 (storage or memory) | 89592 | <u>L8</u> |
| <u>L7</u> (master or central or global or relay) near5 L6 | 14390 | <u>L7</u> |
| <u>L6</u> node | 317036 | <u>L6</u> |
| <u>L5</u> (arbiter\$7 or select\$5 or multiplexer or mux) and 11 and l3 | 4 | <u>L5</u> |

| | | | |
|-----------|--|-----|-----------|
| <u>L4</u> | l2 and L3 | 0 | <u>L4</u> |
| <u>L3</u> | (cpu or (central adj processing adj unit) or microprocessor or microcontroller) with behalf | 363 | <u>L3</u> |
| <u>L2</u> | (arbiter\$7 or select\$5 or multiplexer or mux) same L1 | 147 | <u>L2</u> |
| <u>L1</u> | single near2 bus near2 address | 548 | <u>L1</u> |

END OF SEARCH HISTORY

Refine Search

Search Results -

| Terms | Documents |
|-------------|-----------|
| L7 same L18 | 25 |

Database:

| |
|---|
| US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins |
|---|

Search:

| | |
|-----|--|
| L19 | <input type="button" value="Refine Search"/> |
|-----|--|

Search History

DATE: Sunday, December 19, 2004 [Printable Copy](#) [Create Case](#)

| Set | Name <u>Query</u> | Hit Count | Set Name |
|--|-------------------|-----------|------------|
| side by side | | | result set |
| DB=USPT; PLUR=NO; OP=ADJ | | | |
| <u>L19</u> 17 same L18 | | 25 | <u>L19</u> |
| <u>L18</u> 115 with (processor or node) | | 2407 | <u>L18</u> |
| <u>L17</u> l2 and L16 | | 9 | <u>L17</u> |
| <u>L16</u> l7 same L15 | | 92 | <u>L16</u> |
| <u>L15</u> (rerouting or re-routing or re-directing or redirecting or directing or routing) with (access\$4 or request\$2 or transcation\$2 or command\$2) | | 15692 | <u>L15</u> |
| <u>L14</u> l2 and L13 | | 168 | <u>L14</u> |
| <u>L13</u> l1 and L12 | | 486 | <u>L13</u> |
| <u>L12</u> (re\$1rout\$4 or re\$1direct\$5 or direct\$5 or rout\$5) with (access\$4 or request\$2 or transcation\$2 or command\$2) | | 156974 | <u>L12</u> |
| <u>L11</u> (re\$1rout\$4 or re\$1direct\$5 or direct\$5 or rout\$5) with (access\$4 or request\$2 or transcation\$2 or command\$2) with ((another or other) near1 processor) | | 0 | <u>L11</u> |

| | | | |
|------------|---|--------|------------|
| <u>L10</u> | (re\$1rout\$4 or re\$1direct\$5) with (access\$4 or request\$2 or transcation\$2 or command\$2) with ((another or other) near1 processor) | 0 | <u>L10</u> |
| <u>L9</u> | re\$1direct\$5 with (access\$4 or request\$2 or transcation\$2 or command\$2) with ((another or other) near1 processor) | 0 | <u>L9</u> |
| <u>L8</u> | l4 same l6 same L7 | 53 | <u>L8</u> |
| <u>L7</u> | ((shar\$5 or common) near1 (storage or memory)) | 16006 | <u>L7</u> |
| <u>L6</u> | processors with (coupl\$5 or connect\$5) | 16479 | <u>L6</u> |
| <u>L5</u> | L4 with ((shar\$5 or common) near1 (storage or memory)) | 221 | <u>L5</u> |
| <u>L4</u> | (single or one) near2 (link\$4 or bus) | 101305 | <u>L4</u> |
| <u>L3</u> | l1 same L2 | 14 | <u>L3</u> |
| <u>L2</u> | address adj bus | 24993 | <u>L2</u> |
| <u>L1</u> | processors with (coupl\$5 or connect\$5) with ((shar\$5 or common) near1 (storage or memory)) | 730 | <u>L1</u> |

END OF SEARCH HISTORY

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Term: L8 not 17

Display: 15 Documents in **Display Format:** - Starting with Number 1

Generate: Hit List Hit Count Side by Side Image

Search History

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| <u>Set</u> | <u>Name</u> | <u>Query</u> | <u>Hit Count</u> | <u>Set Name</u> |
|------------|--|--------------|------------------|-----------------|
| | side by side | | | result set |
| | DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ | | | |
| <u>L9</u> | L8 not 17 | | 7 | <u>L9</u> |
| <u>L8</u> | l5 and l1 | | 9 | <u>L8</u> |
| <u>L7</u> | l5 and L6 | | 2 | <u>L7</u> |
| <u>L6</u> | L1 with (processor or node) | | 84 | <u>L6</u> |
| <u>L5</u> | l2 same L4 | | 6876 | <u>L5</u> |
| <u>L4</u> | processors or nodes | | 312072 | <u>L4</u> |
| | DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ | | | |
| <u>L3</u> | l1 same L2 | | 3 | <u>L3</u> |
| <u>L2</u> | (shared or common) near1 (memory or storage) | | 31880 | <u>L2</u> |
| <u>L1</u> | (routing or directing or re-routing or rerouting or redirecting or re-directing) near5 request | | 867 | <u>L1</u> |

END OF SEARCH HISTORY